

EE 271-Experiment # 5:
Digital-to-Analog Converter (DAC) R-2R Ladder Network

Table 1. Predicted and measured output voltage values.

S₃	S₂	S₁	V_{out} (predicted) (V)	V_{out} (measured) (V)	Error (%)
0	0	0			
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	0			
1	1	1			